

ABSTRACT OF THE DISCLOSURE

An integrated circuit device adapted to be incorporated into a portable article having a memory, in particular of a card format. The device includes a central processor unit, at least one memory, at least one data input/output pad, n address bus lines connecting the central processor unit to the memory and/or to the input/output pad to carry address bits, and p data bus lines connecting the central processor unit to the memory and/or to the input/output pad for conveying data bits. At least one address line from the address bus and the data bus is associated with an additional line for conveying bits that are complementary to the bits conveyed over the address bus line or data bus line.